

2 $t = \left(\frac{d-1}{2}\right) =$ For perfect input l 1 fault during need ideal decoding circuit, we of output ideal decoding of input. Another way of saying this: We want to construct a circunt that Jen's with probability O(p2) il it can deal with all snife qubit evos (assung id voix).

3 Good ever Pareli X ervor え Ċ Very metal: CNOT X & X XØ ° −) ° CNOT I e X 6 X ONOT 20 201 CUST 102 202 -) -

Bad error 4 But X 下, 5-) - X - HI-A 1 ancilla error led 10 (an2 errors on output un correctable error)

(5) One solution [Shor '94] Shor EC Instead of using a bare ancilla use a cert state. 10) on + 11) on (omit normalization) Xaxaxax measurement +1 for 10000)+11111) 1 pr 100007 - 11111)

Now any single fault during 6 the circuit can only lead to at most one familt on the ortput. But how do me prepare the cat states fault-tolerantly? Cat state is a stabilizer state (studilizer code w/ k=0) 10000)+(((1)) 2, 24, 2,22,223, is stubilized by $X_1 X_2 X_3 X_4$

7 Verification circuit stabilizer eigenvalues Cheet Cut state 10) - H + (0) 16) ______ 10700-17 noisy cat preparation verfication Accept if measurement result 15+1, reject otherwise. Remember as on code hers d=3 we are only wanted about single faults.

The verification circuit 8 catches all X enus on the imput ant states. It is possible four single qubit X enors to be moduced nots the cast state dring the vertication part, but have the same effect as sugle gubit X errors on the cat state churg the measurement of the stabilizer.

9 We don't measure the stabilizer. X, X2 X3X4 So single qubit 2 enors can occur meaning that we will have 100007-11111) instead of 10000)+ 11111) This could cause us to apply the wrong concertion as the measurement outcome would be Hipped.

To deal with this we repeat (10) the whole procedure 3 thes and take the majority role for the stabilizer measurement ontcore. As we are only considering supe faults dury the entire proceedre, we will get an acurate result.

 (\mathbb{N}) This whole proceedie is ratter complicated and required w+1 ancillers nhere w is the weight of the stabilizer we want to meare. Cen ve do better? Yes, my Hag qubits

(12)Flag envor correction [Chao, Reichardt 2018] First, another way he measure ZO4 147 107 DOD B Can be derived from our prenions circuit by inserting I = HH

Idea: add'flag' ancilla to (13) catch bad faults where 1 janlt -> 29 fault on derter gubits 17 $\phi \phi \phi \phi \phi$ -17 10) -H-M 1+7 1 Flag



In general, for Henry EC (15) we only need 2 extra Hay qubits for femilt tourant stabilizer measurement. Aside: In the (20) surface code, these construction are not necessary & familt tolerance cen be actieved by repeating the stabilizer measurements O(d) they where d is the code distance.

(16)FT Measwement There exists a procedure for genori stabilizer codes ont it is rather cumberone so we will consider the special case of <u>CSS</u> codes Recall that CSS codes are constructed from two classical lineer codes CiEn, ki, d, J CiEn, ki, d. J

where $\mathcal{C}_{1} \subseteq \mathcal{C}_{2}^{\perp} \notin \mathcal{C}_{3}^{\perp} \oplus \mathcal{C}_{3}^{\perp}$ where $\mathcal{C}_{1} \subseteq \mathcal{C}_{2}^{\perp} \notin \mathcal{C}_{3}^{\perp} \oplus \mathcal{C}_{3}^{\perp} \oplus$ $CSS(\mathcal{C}_1, \mathcal{C}_2^{\perp})$ hers parameters [[n,k,-k2,d]] $d = min(d_1, d_2)$ $e_1 \times stabilizes$ $e_2 \pm z \times stabilizes$ The important fact fer us is the form of the coden arels lv+w) $|\bar{v}\rangle = \bar{Z}$ $w \in C_2^{\perp}$ Suppose we want to measure logical Z for all encoded qubits

(18)To do this me measure all the gubits in the Z basis. The only errors he need to warry about cre toit-thips as phase-Hips won't charge the measurement outcouls Let e E F2 reperent a tit-flip ever, metry our state Z (v+w+e) w+er

When we measure we will (19)
observe the outcome
v+w+e for vandom w l2
Now, vtw is a codeword
of ℓ_1 as $v \in \ell_1$ $c \ell_2^{\perp} \subseteq \ell_1$
Suppose $ e \leq t = \left\lfloor \frac{d-1}{2} \right\rfloor$
We know that d>, min (d,, d2)
Thefere we can just vur
the classical decoding algorithm
for e, to obtain vtw l hence v.

20 FT State prep Roblem: naively implementing the encoding circuit is not FT l.g. [[4,2,2]] vode S = (X X X X, 2222) $\overline{X}_{i} = |XX|$ $\overline{X}_{1} = XXII$ $\overline{z}_1 = |\overline{z}_1| = \overline{z}_2 = \overline{z}_2(1)$ with Can pepse 106) the following avant

(21)10) 7 10) 167 € (+) e.g Not 10) 0) 167 Æ 14 fault 1 -> 2 faults on ontput

One can use flag type (22) tricks agent but fer CSS codes there is a vice general method for FT state prep, Task pupoe 10.0) all logical state zeros 10-0) Z w) E Sloyen StSX & X stabilizes ez describes X stubs

23 Z S 10)⁶⁴ SESX 10.0) $\overline{11}(1+g)(0)^{6n}$ gi 2 where Sx = (g1, g2, ..., gm) X stabilizer generating Set for the is simply a projective (1+gi) measurement of the generator gi. We can do this fault-tolerantly using e.j.

24 Shor EC So to pepare 10.0) we measure a generatry set of the X stabilize group l then apply the appropriate (2) recovery questor 1+ doesn't matter if we apply a logical Z ws will be a product of Z TT (1+gi) 10700 Z ups in a CSS $g_{i} = \frac{1}{1} (1+g_{i}) \overline{Z} \log^{6} n$ code. gi z 10700

Post script · Otter FT stabilizer measurement protocols exist, notably those of sterre 9 Kmill. · CSS codes we by far the most popular class of codes because of their nice FT properties, and in fact any [[a, k, d]] Stubilizer code can be mapped onto a [[4n, 2k, 2d]] CSS code.